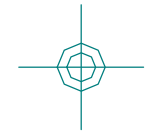
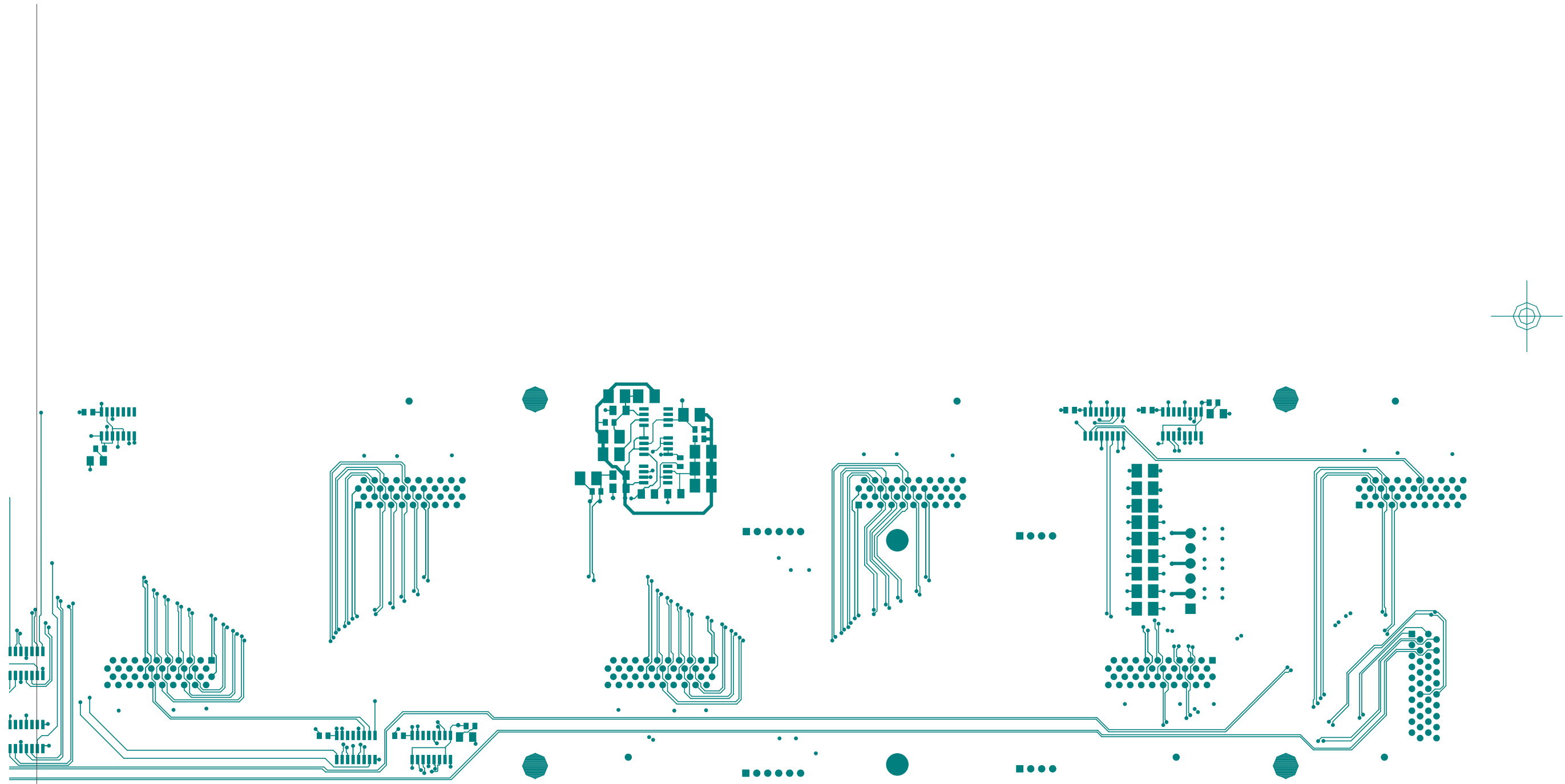
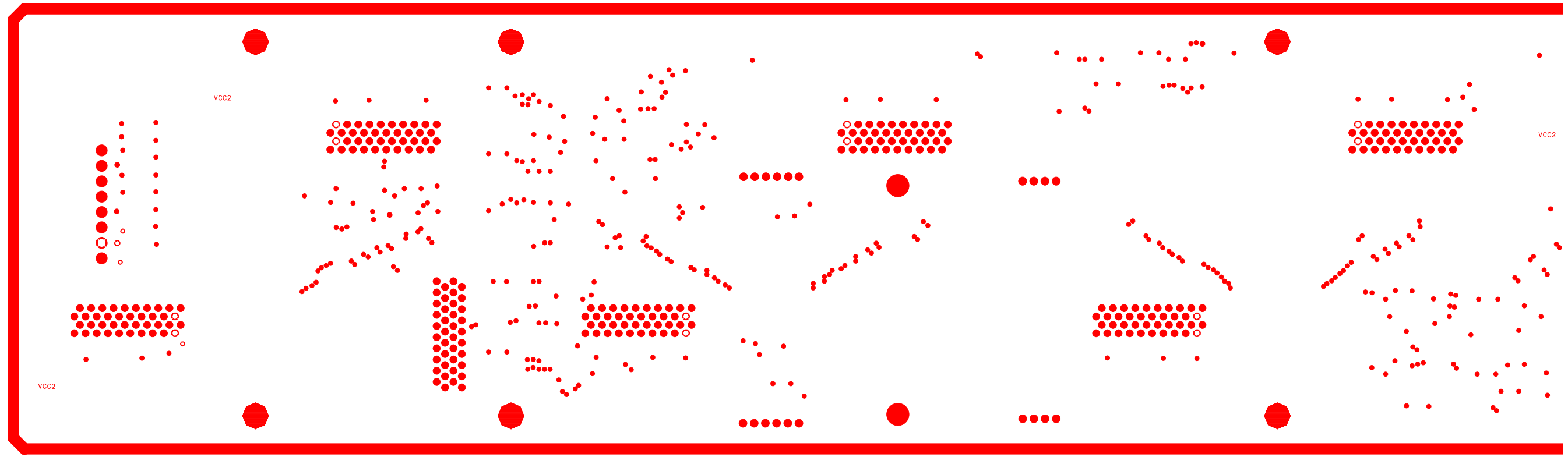
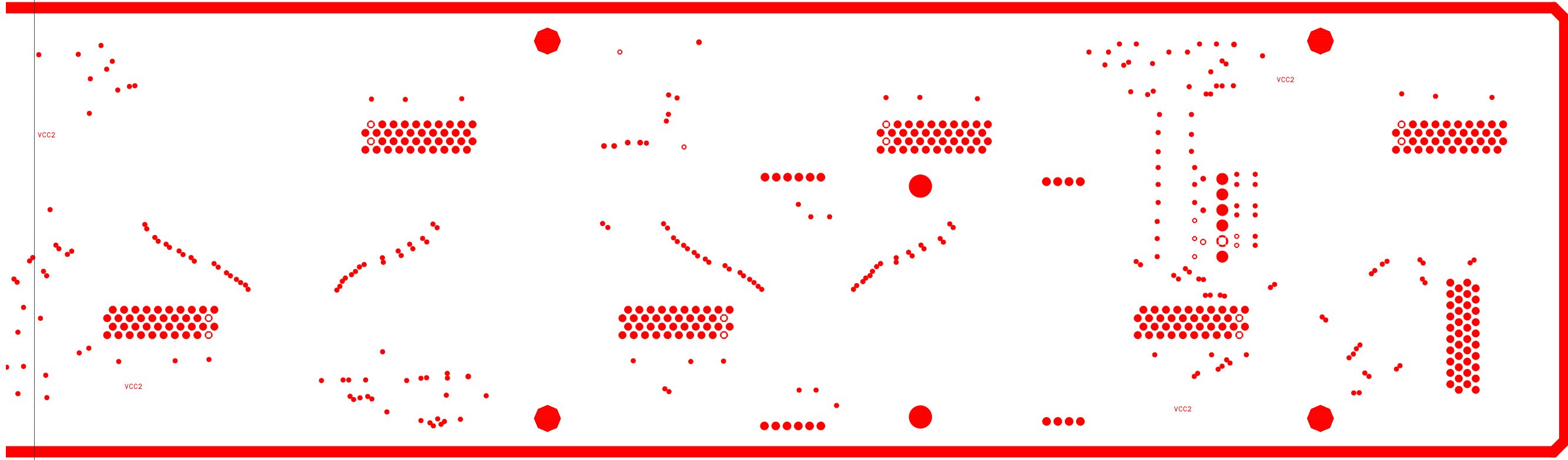
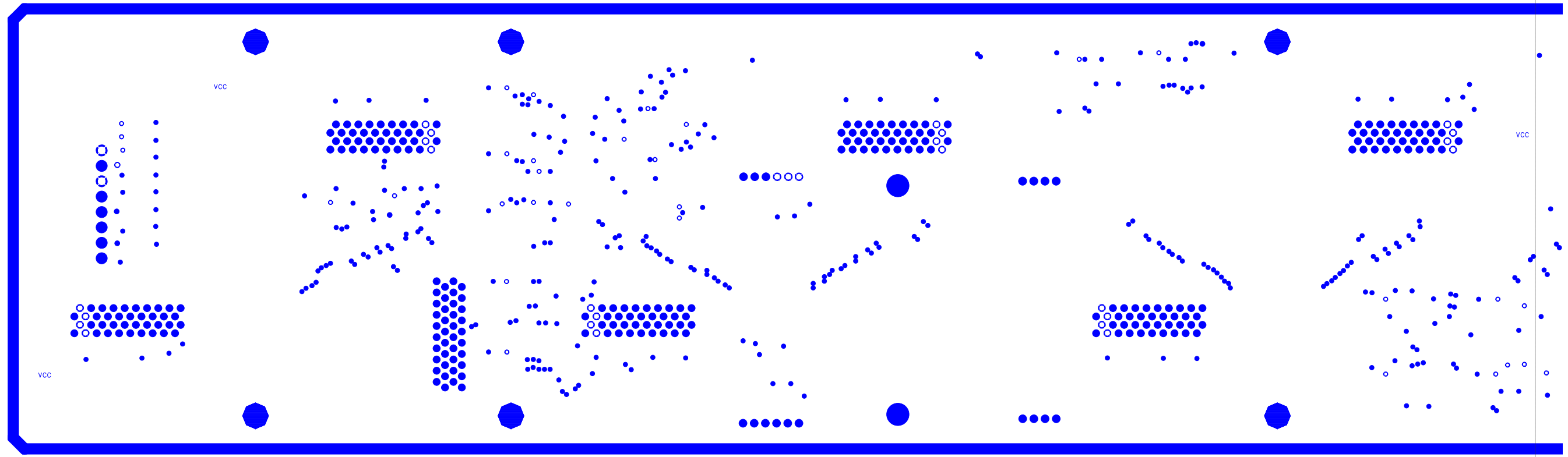


FILM1: Top Component Layer (SIGNAL_1)

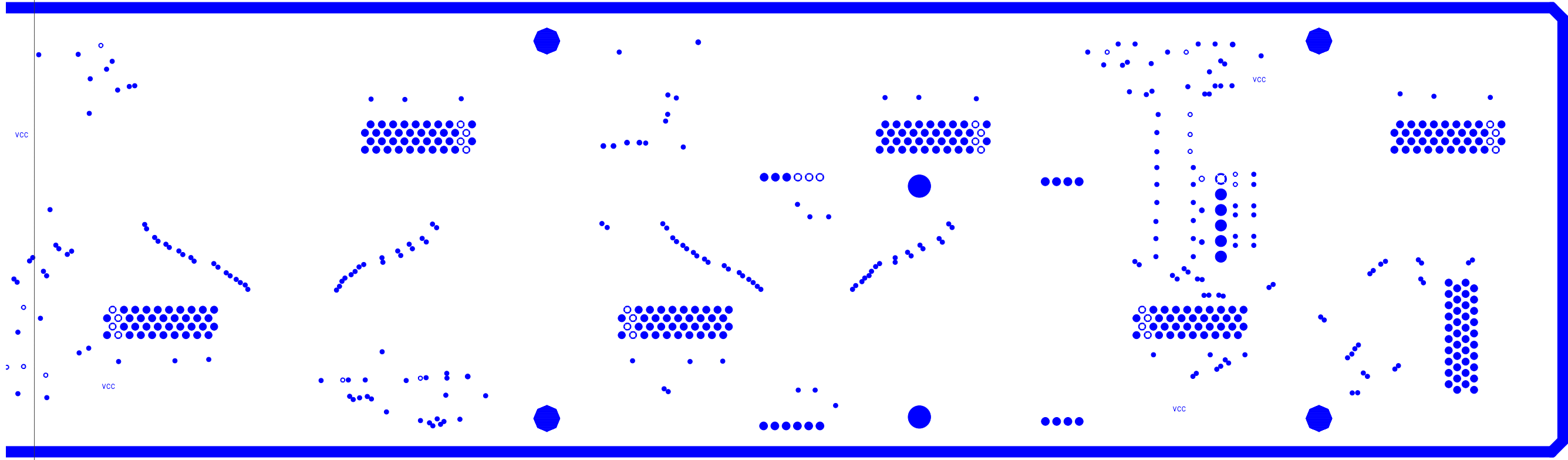


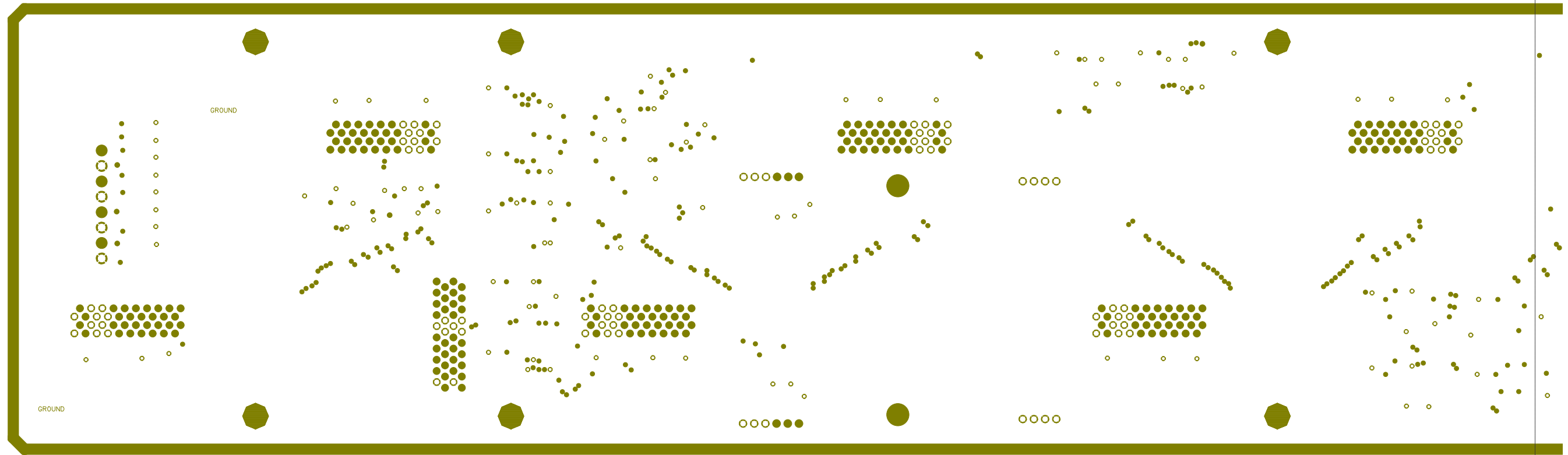


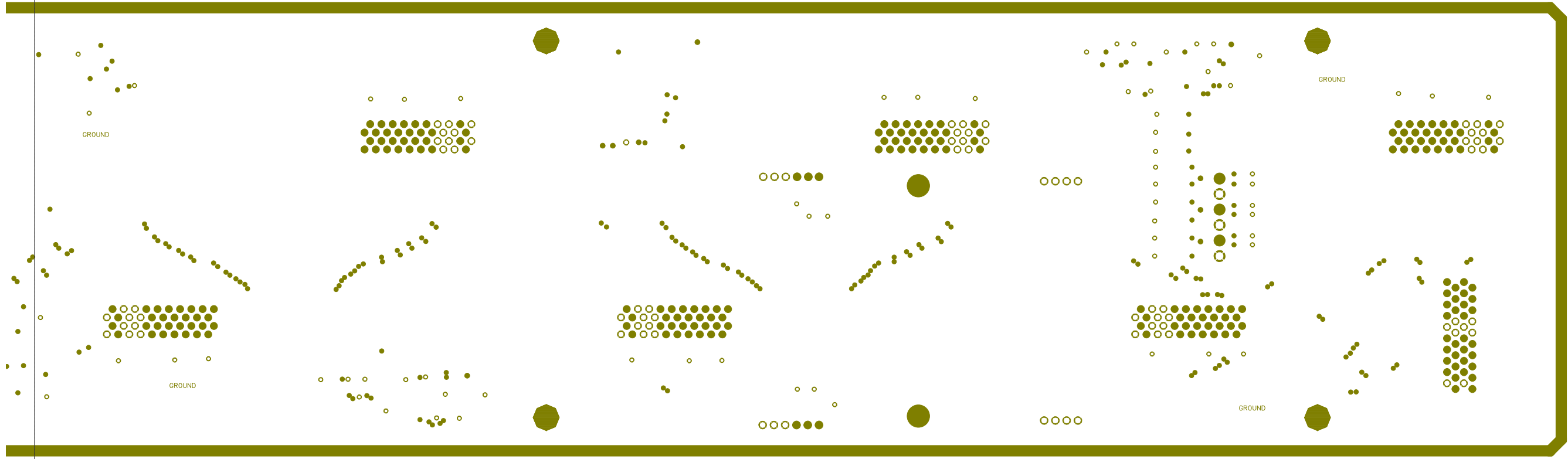


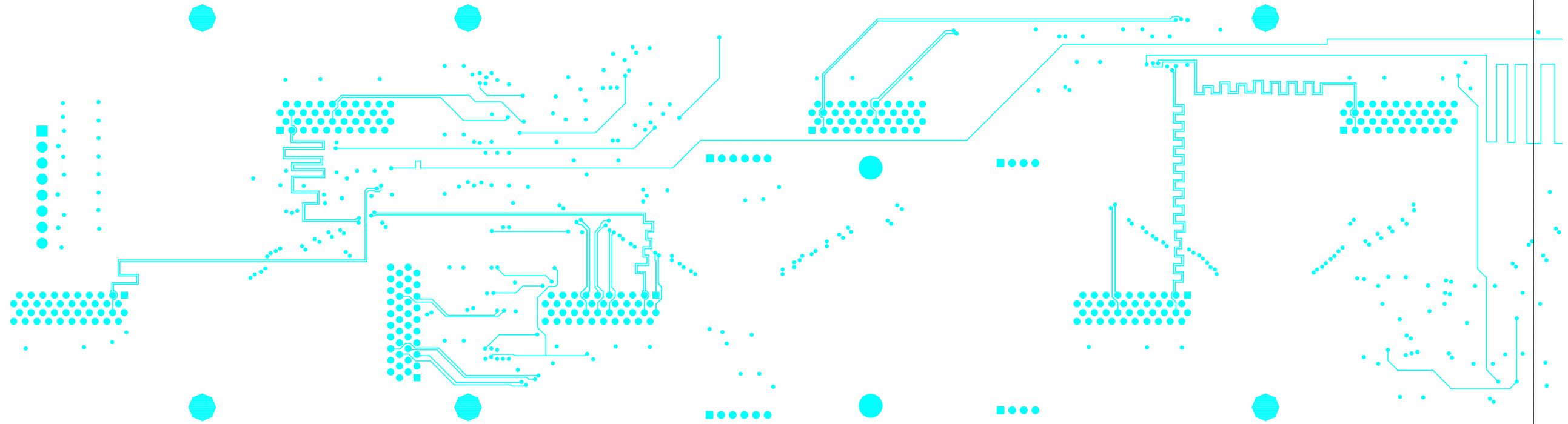
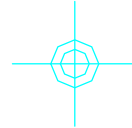
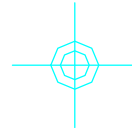


FILM3:Power Plane (VCC)

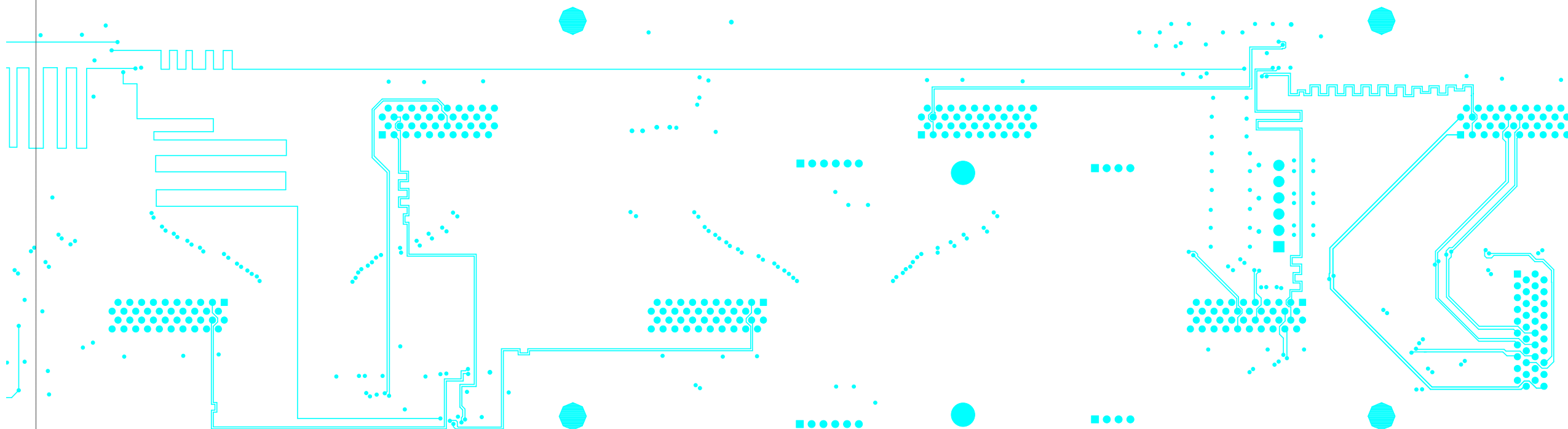
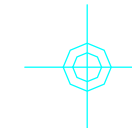


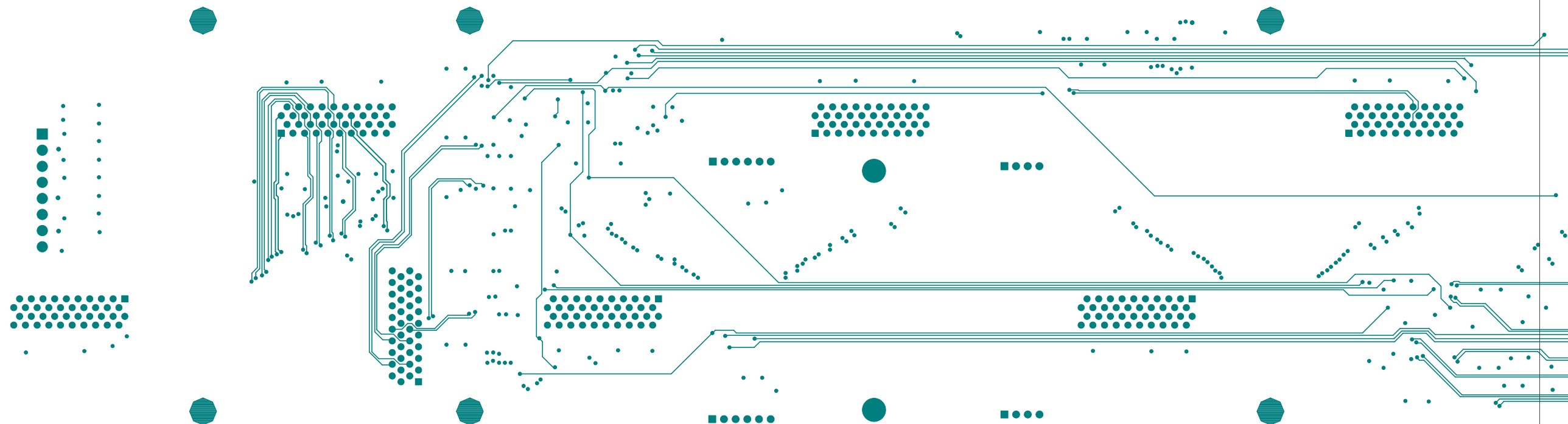
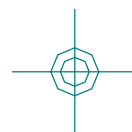




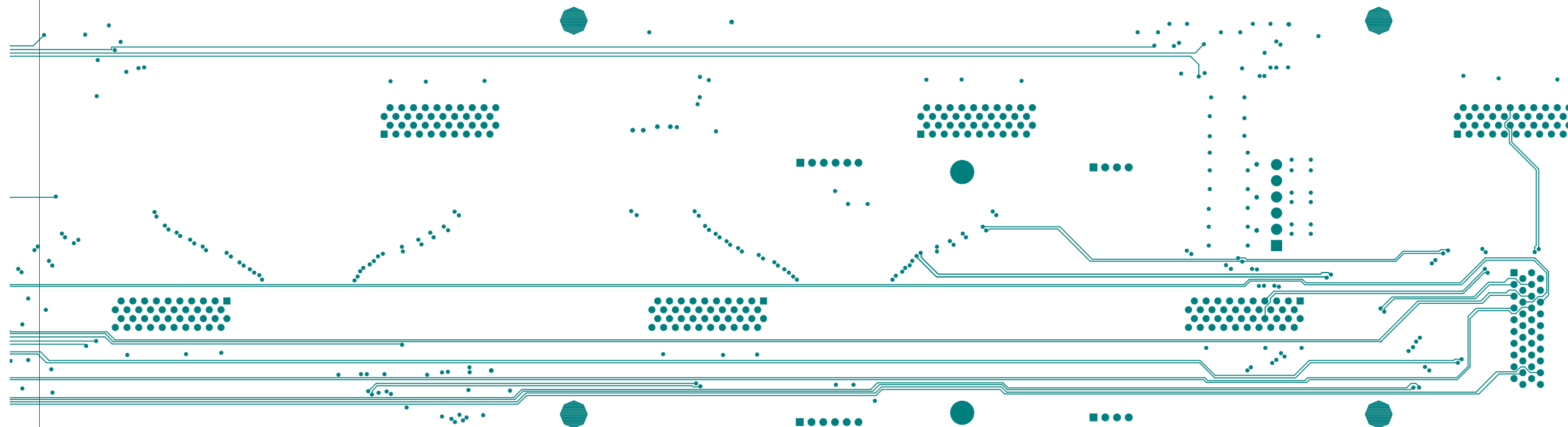


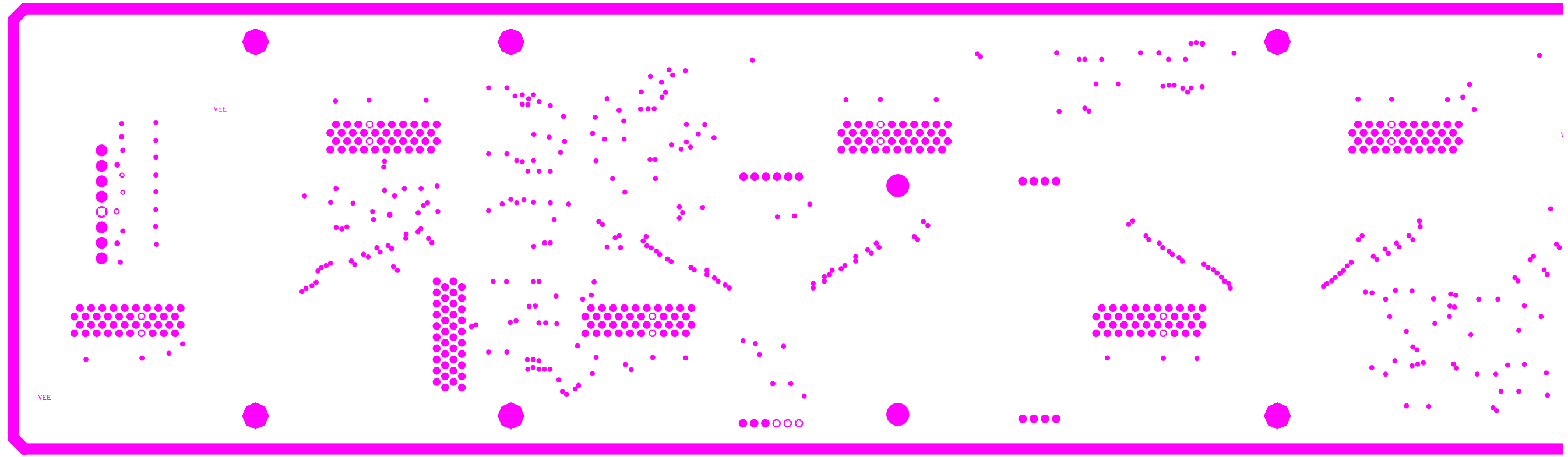
FILM5: Inner Signal Layer (SIGNAL_3)

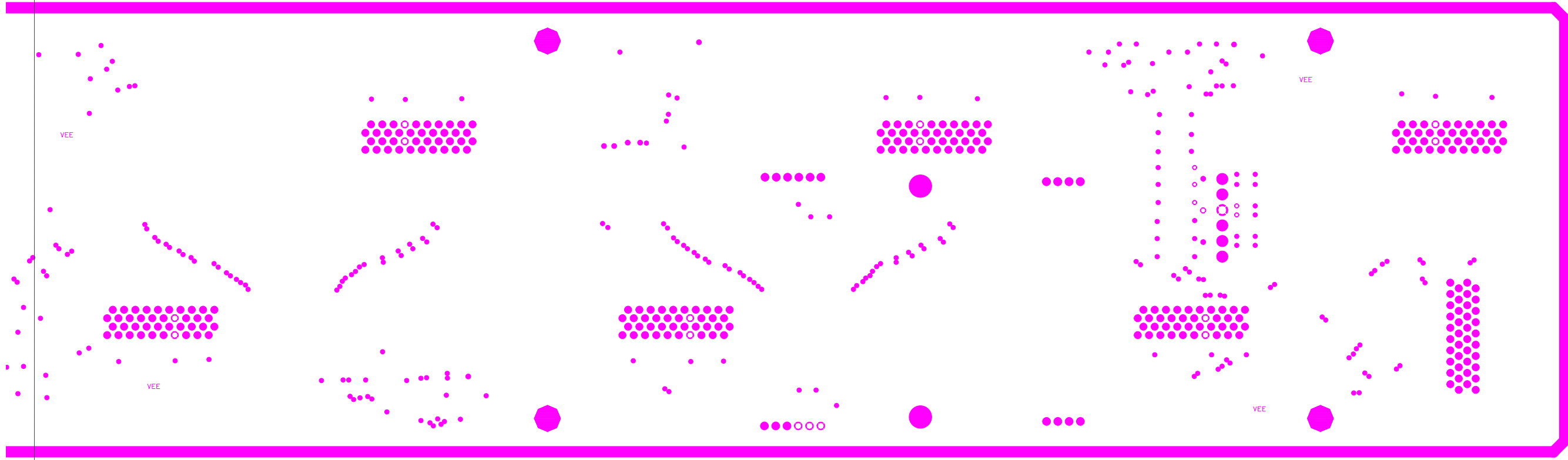


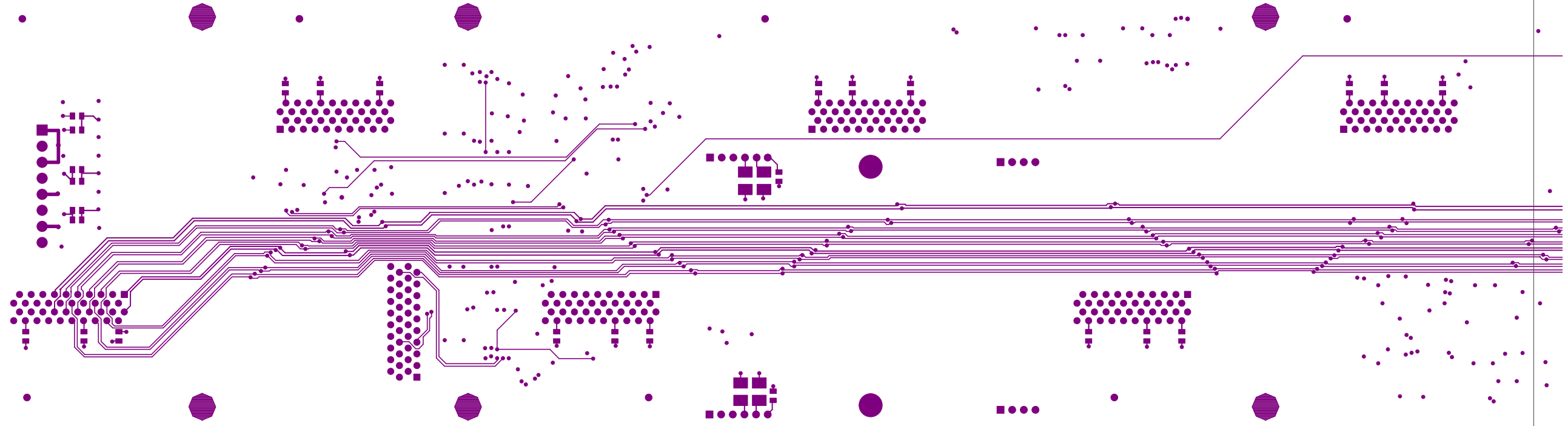
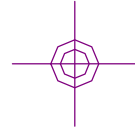
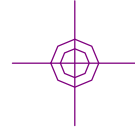


FILM6: Inner Signal Layer (SIGNAL_4)

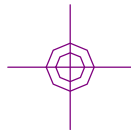
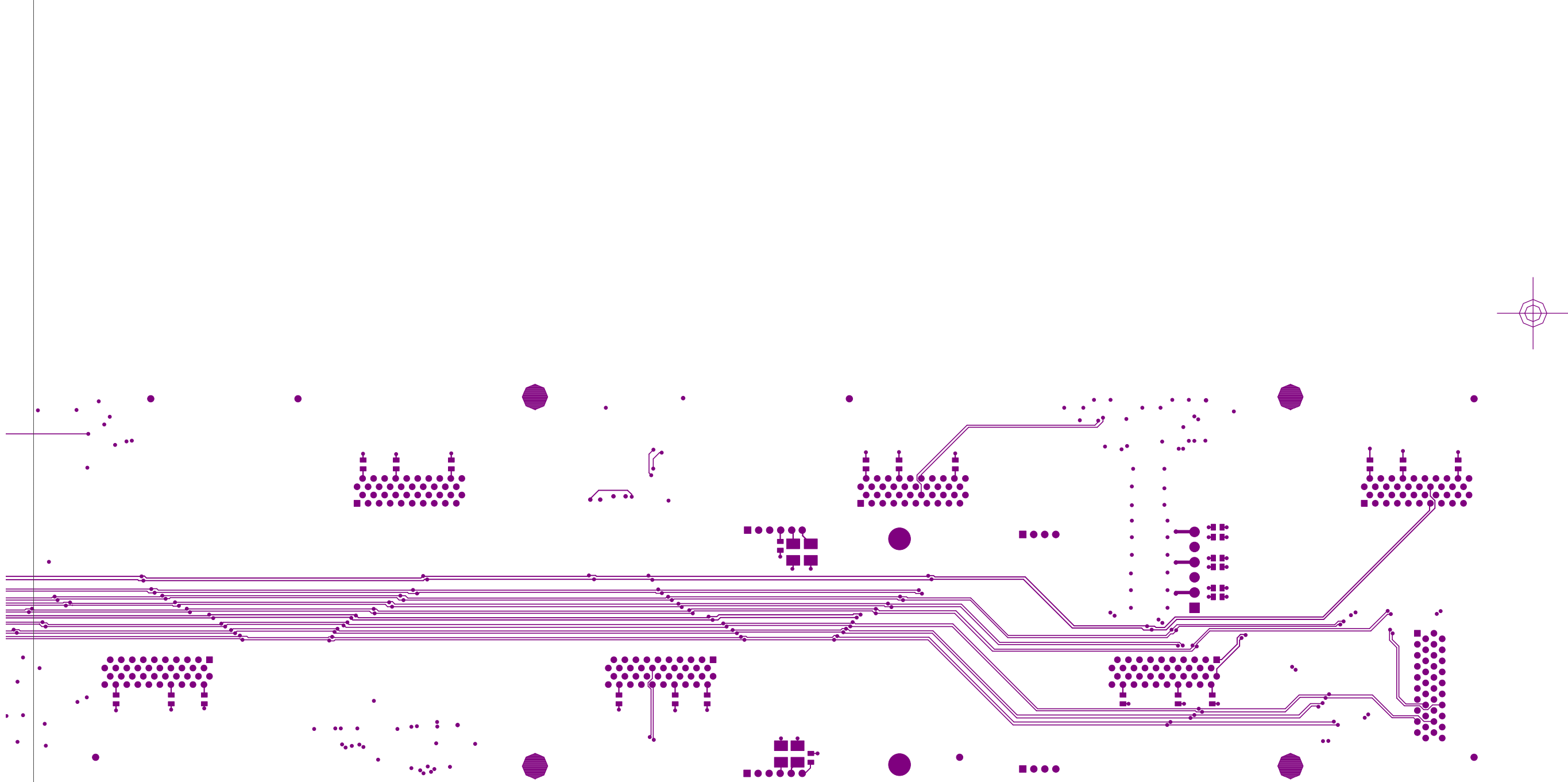


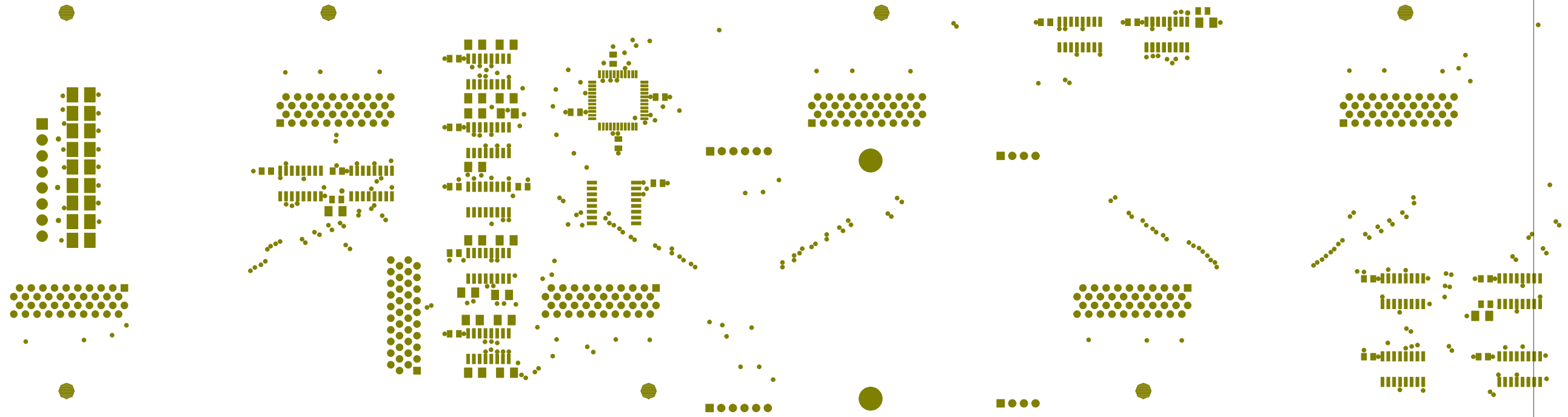
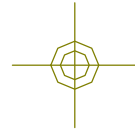




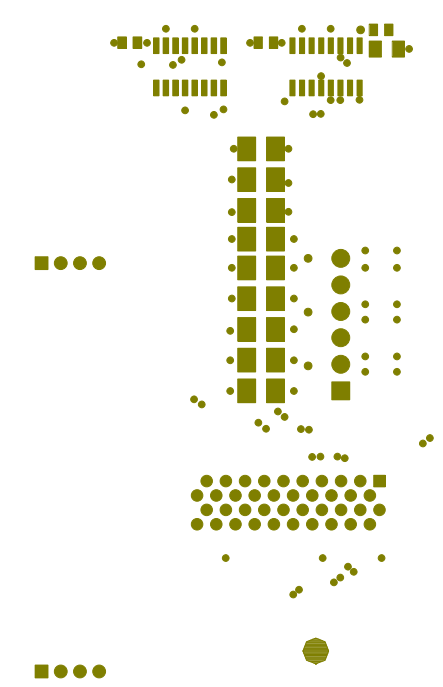
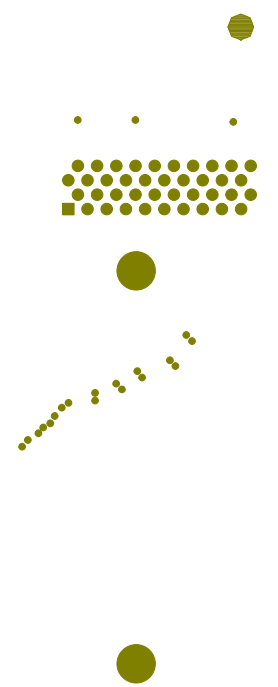
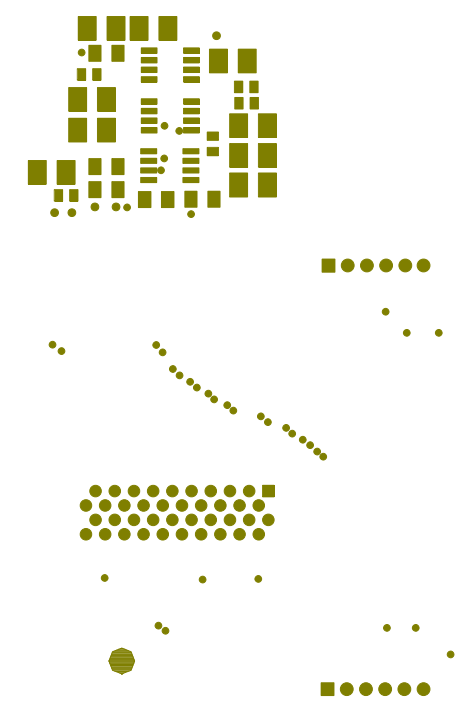
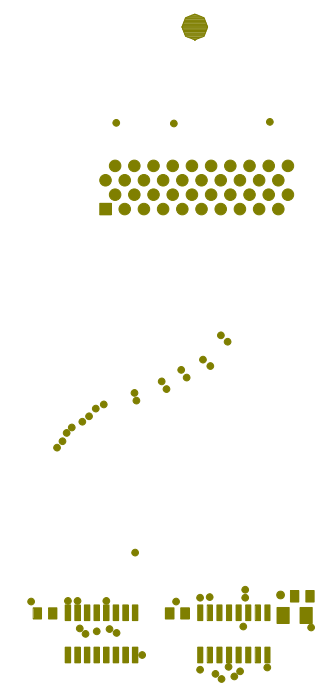
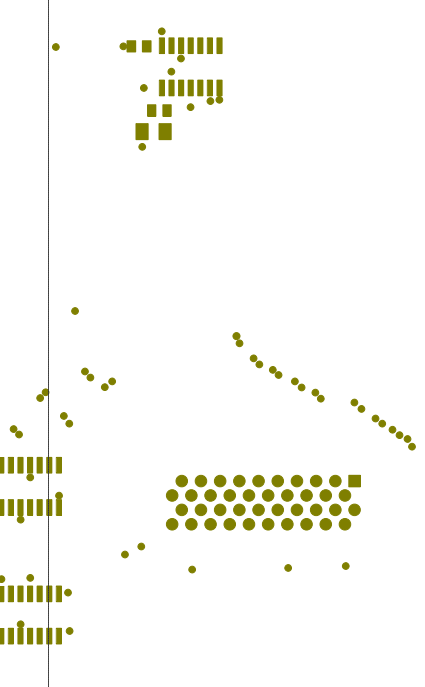


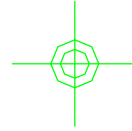
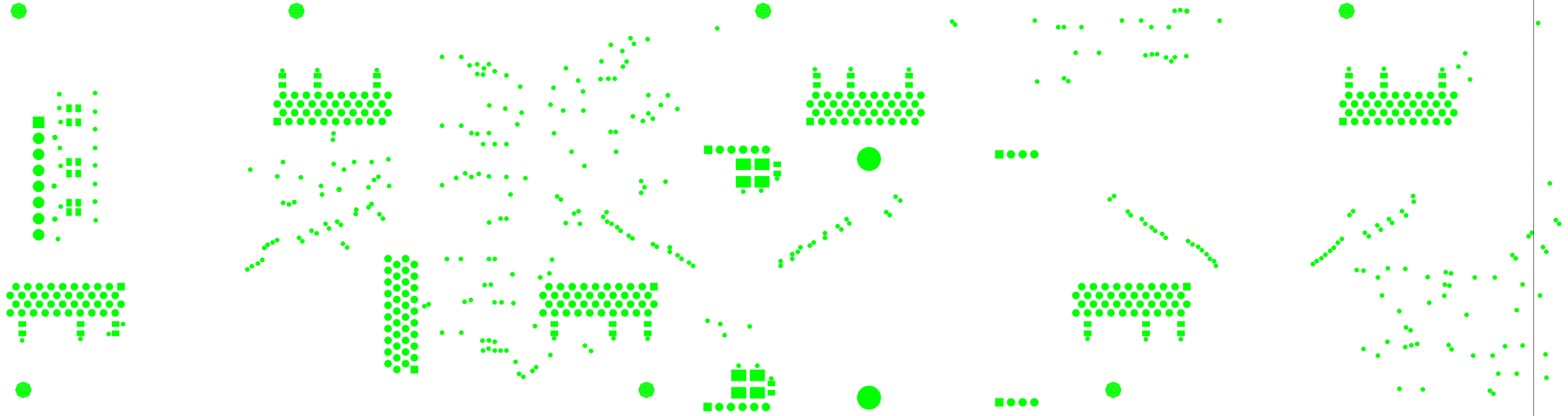
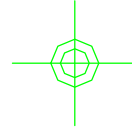
FLM8 : Bottom Component Layer (210MIL_5)



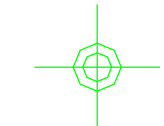
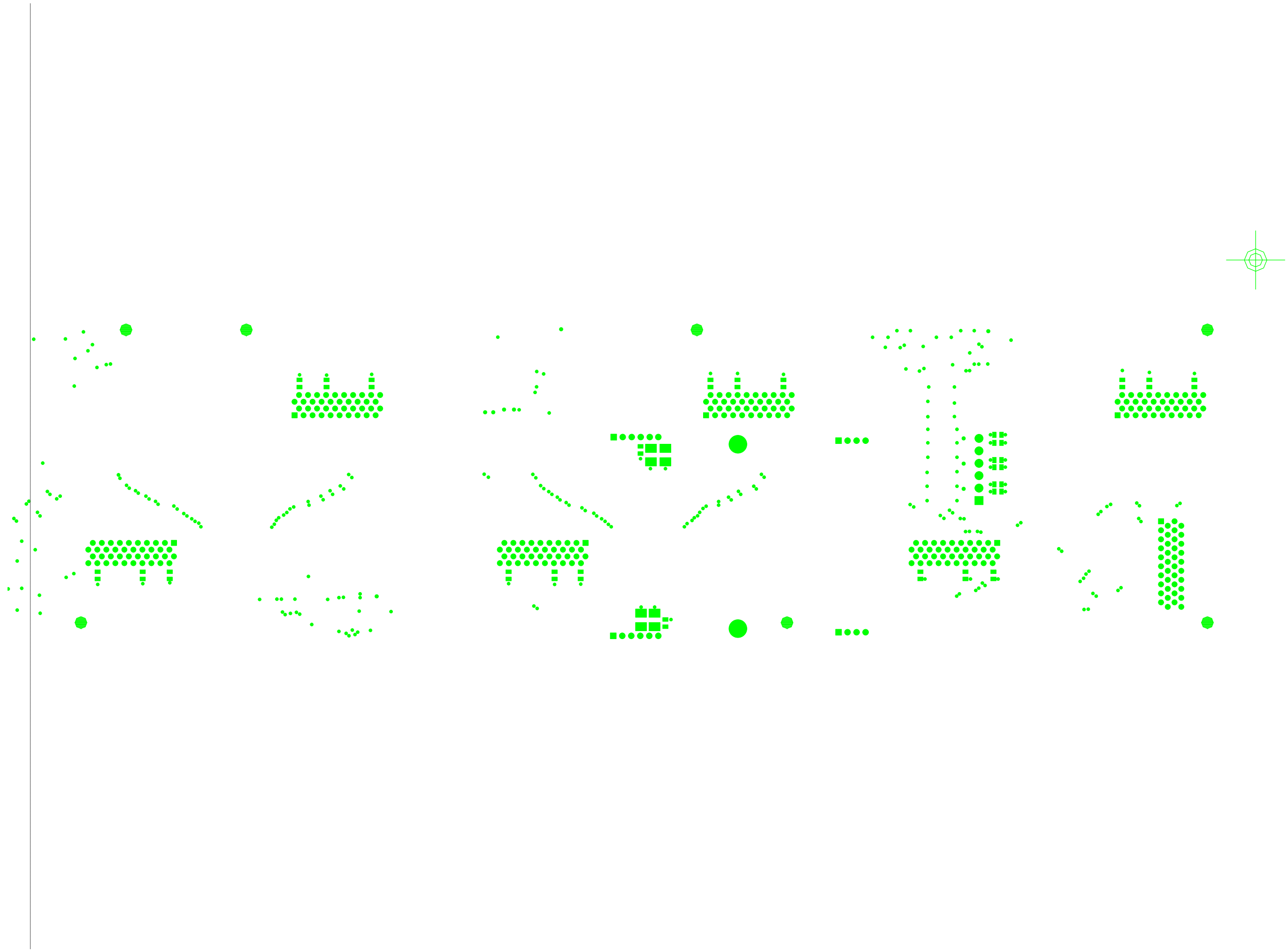


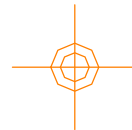
FILM9: Top Solder Mask (Solder_mask_1)





FLM10: Top Solder Mask (S_ktop_mask_5)

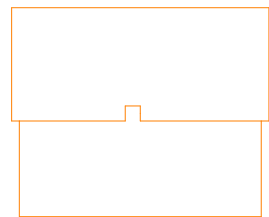
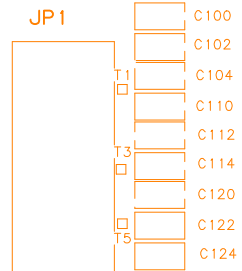




MOTHERBOARD SECTION-3

BAR CODE

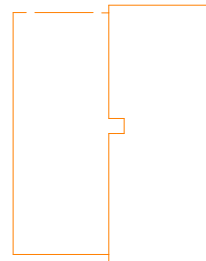
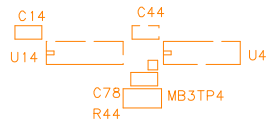
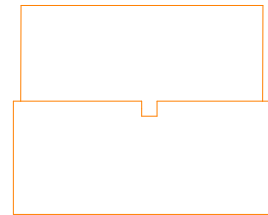
UNIVERSITY OF CHICAGO
DECEMBER 1999



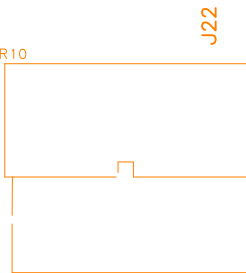
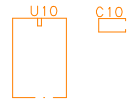
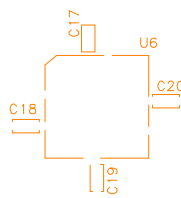
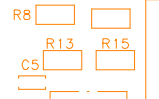
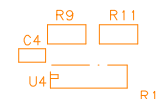
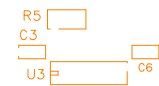
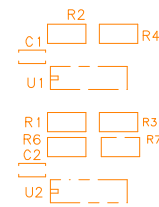
J24

SCH# B2315 V3.1
SPC# B2316 V3.1
ASM# B2317 V3.1

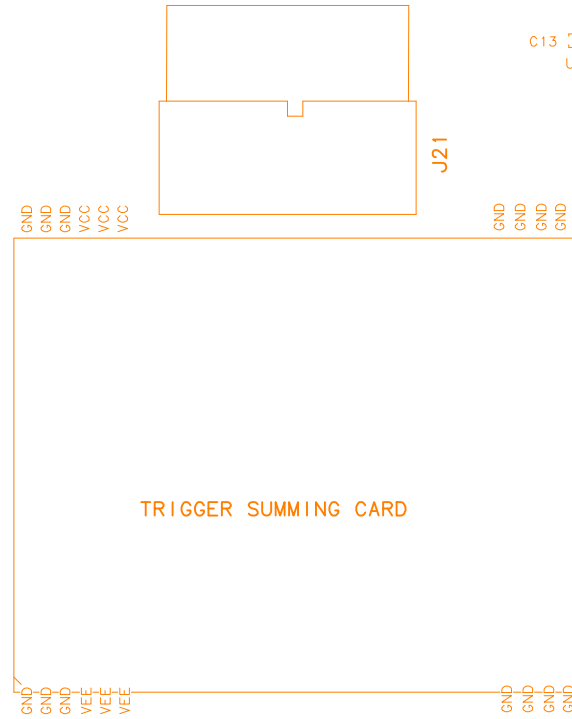
J23



EJ0

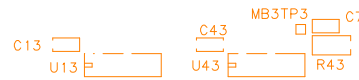
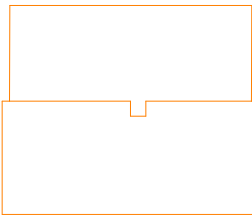


J22

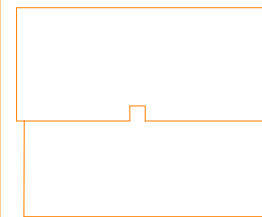


TRIGGER SUMMING CARD

J21

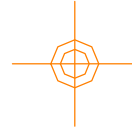
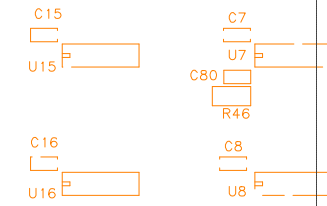
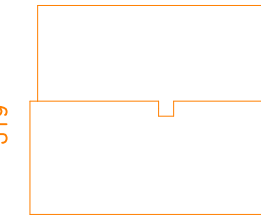


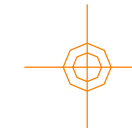
TJ5
TRIGGER SUMMING CARD



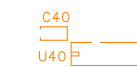
J20

J19

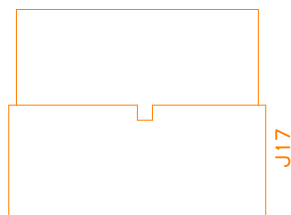




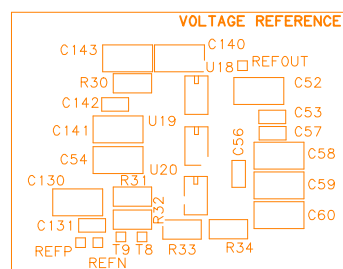
MOTHERBOARD SECTION-3
UNIVERSITY OF CHICAGO
DECEMBER 1999



SCH# B2315 V3.1
SPC# B2316 V3.1
ASM# B2317 V3.1

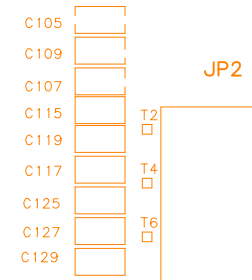
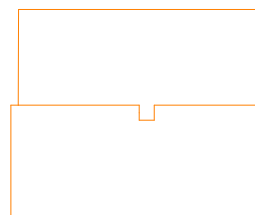


J17



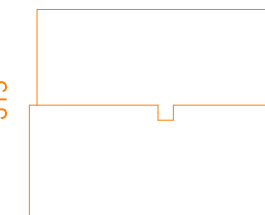
TJ4
TRIGGER SUMMING CARD

J15



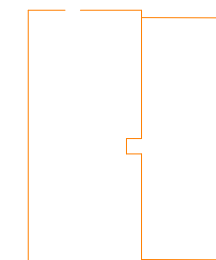
JP2

J13

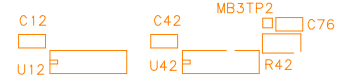


MOTHERBOARD SECTION-3
UNIVERSITY OF CHICAGO
DECEMBER 1999

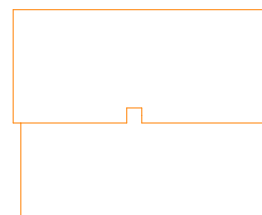
SCH# B2315 V3.1
SPC# B2316 V3.1
ASM# B2317 V3.1



EJ1

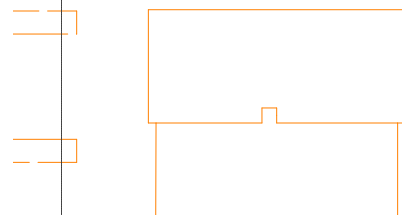
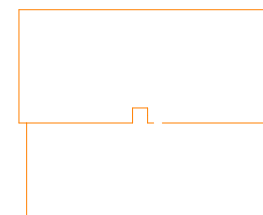


J16

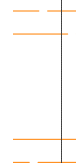


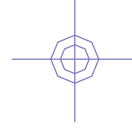
TRIGGER SUMMING CARD

J14



J18





C101
C103
C111
C113
C131
C133

C201
C205
C203

W2M# B5312 A3.1
2PG# B5318 A3.1
SCH# B5315 A3.1
DECEMBER 1999
UNIVERSITY OF CHICAGO
MOTHERBOARD SECTION-3

C308
C302
C304

C303
C308
C306

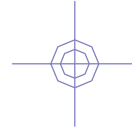
C302
C303
C304

C301
C305
C300

C315
C311
C310

C313
C314
C312

C318
C317
C316



FILM12: Bottom Silkscreen (Silkscreen_5)

DECEMBER 1999
UNIVERSITY OF CHICAGO
MOTHERBOARD SECTION-3

V2M# B5317 V3.1
SPC# B5316 V3.1
SCH# B5315 V3.1

C218
C230
C231

C234
C233
C235

C232
C236
C237

C506
C508
C509

C509
C510
C511

C320
C336
C338

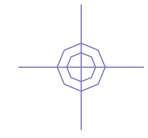
C231
C235
C232

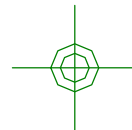
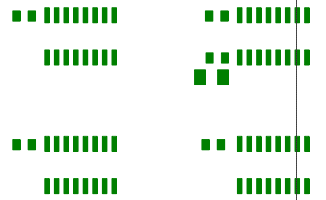
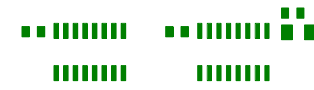
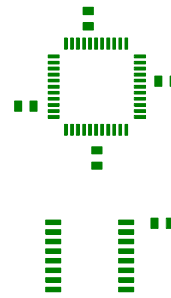
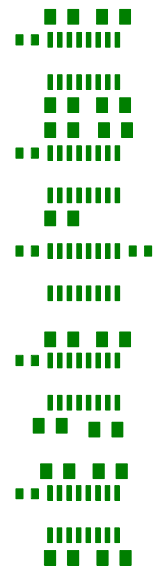
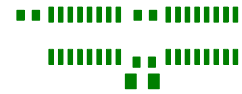
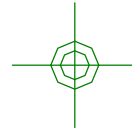
C158
C138
C118
C108

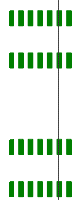
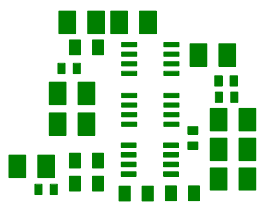
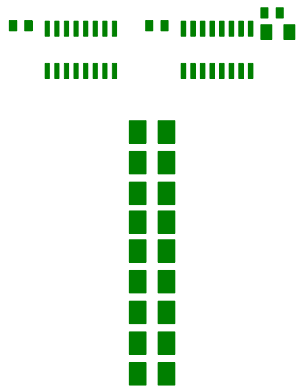
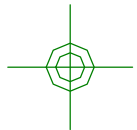
DECEMBER 1999
UNIVERSITY OF CHICAGO
MOTHERBOARD SECTION-3

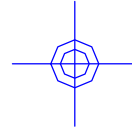
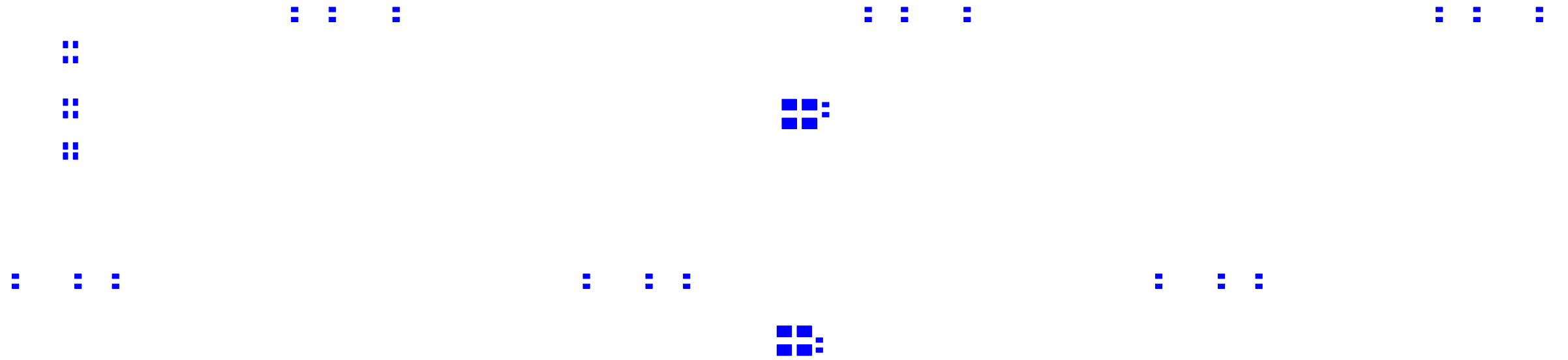
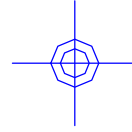
V2M# B5317 V3.1
SPC# B5316 V3.1
SCH# B5315 V3.1

A230
2E20
9E20

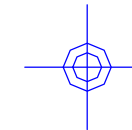








FILM14: Bottom Paste Mask (Paste_Mask_2)



⋮ ⋮ ⋮

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⋮ ⋮ ⋮

⋮ ⋮ ⋮

