
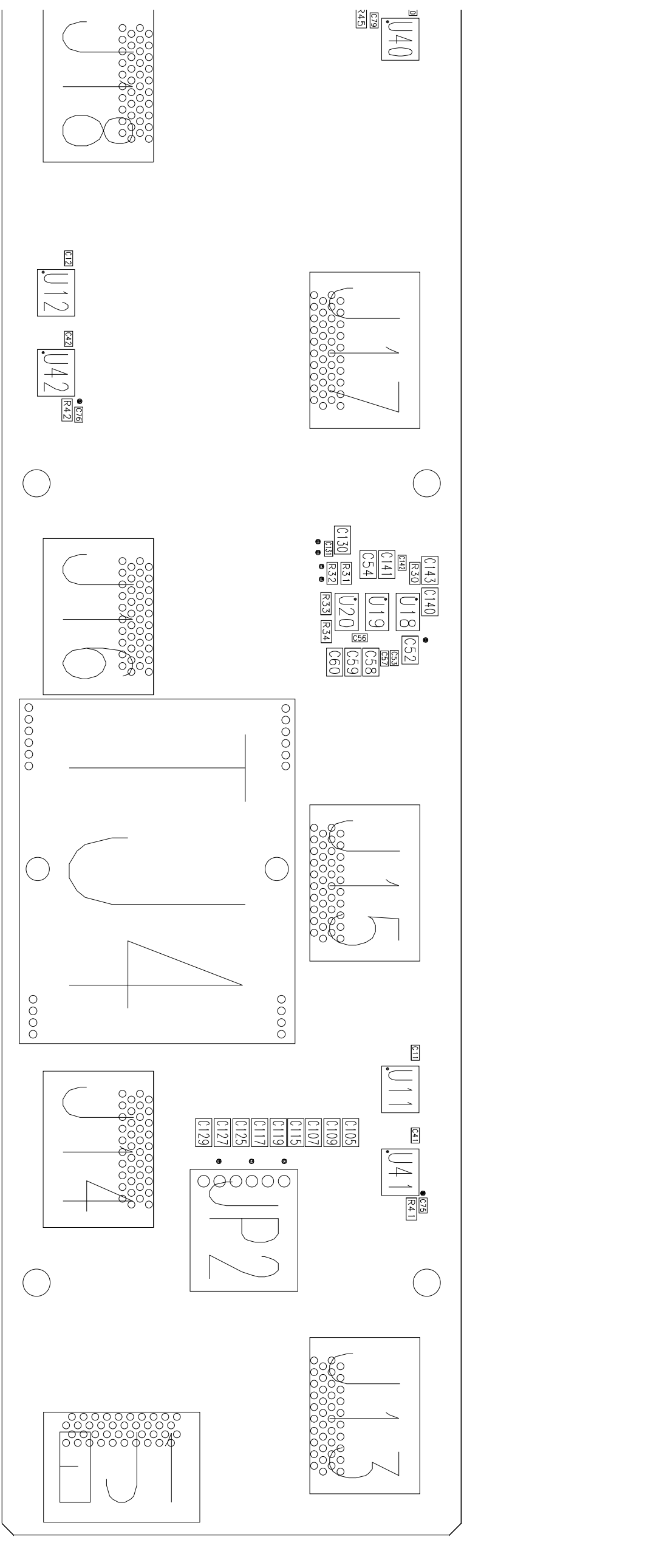


BOARD ORIGIN (0,0)

NOTE:  Pin-1 of ICs

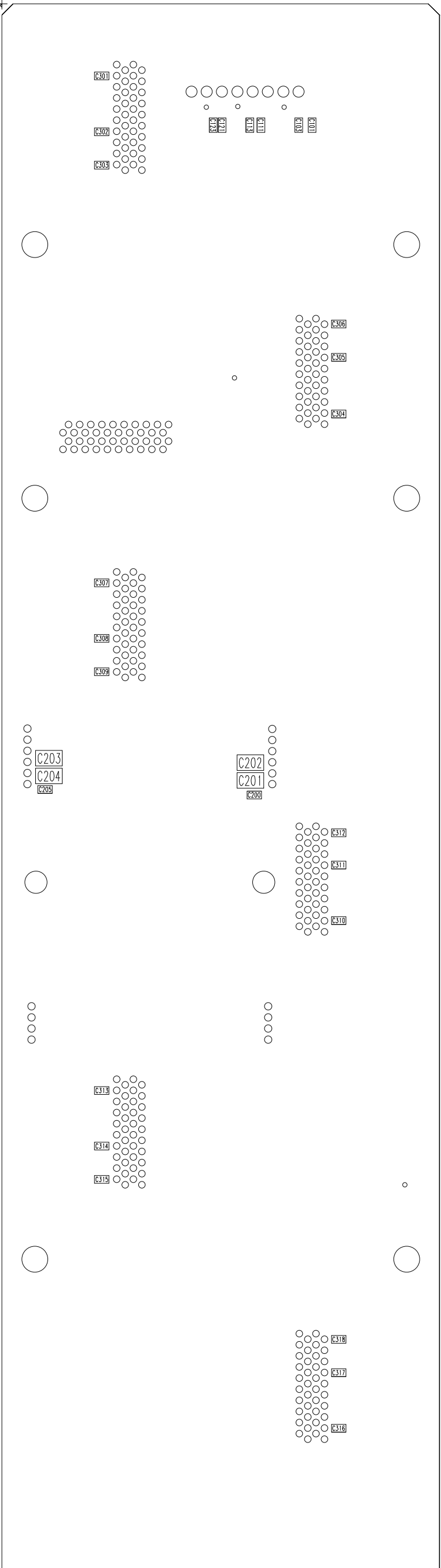


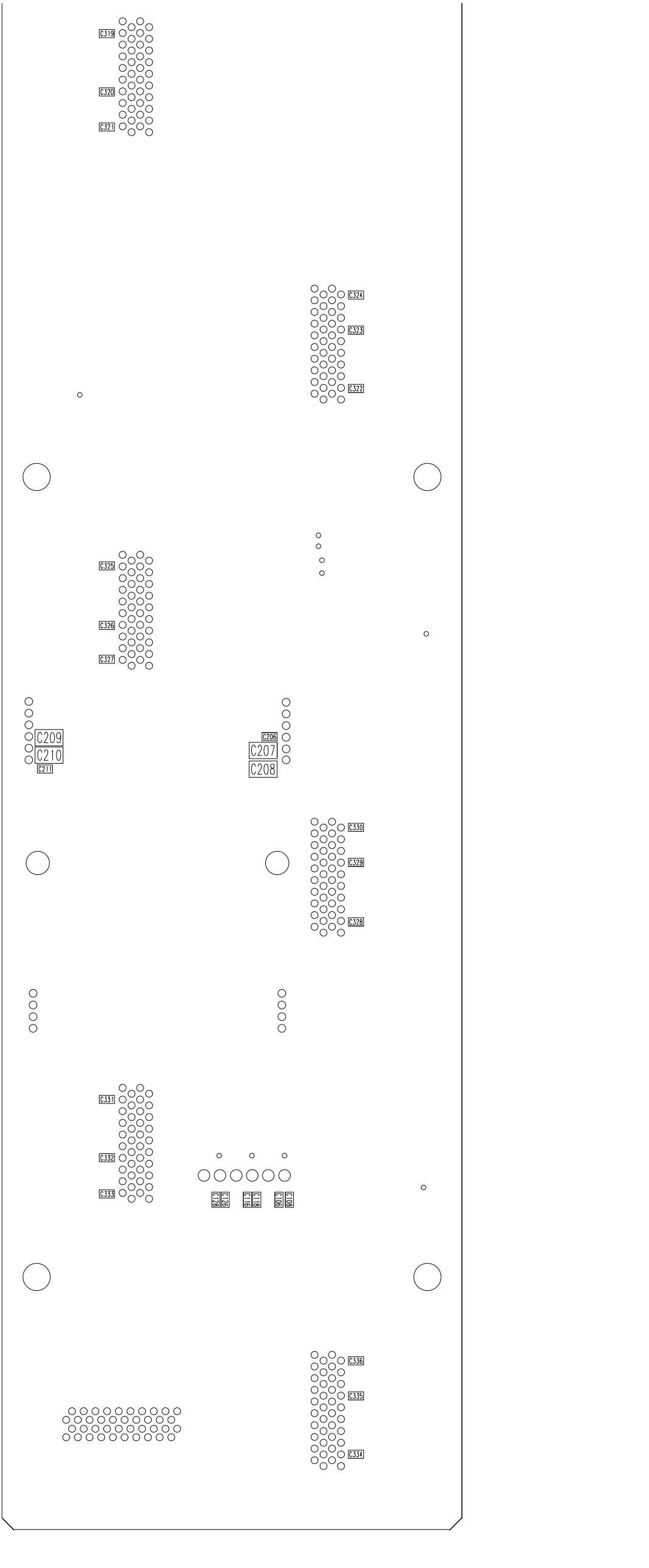
-3 Top Side Placement

SCHM# B2315 V3.1
 SPEC# B2316 V3.1
 ASSM# B2317 V3.1

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE	
B2316 TOP SIDE ASSEMBLY DRAWING	
SHEET	1 OF 2
DATE	05-08-2000
DRAWN	TANG
	B-2317 REV 3.1

BOARD ORIGIN (0,0)





-3 Bottom Side Placement

SCHM# B2315 V3.1
 SPEC# B2316 V3.1
 ASSM# B2317 V3.1

UNIVERSITY OF CHICAGO ELECTRONICS DEVELOPMENT GROUP	
TITLE	
B2316 BOTTOM SIDE ASSEMBLY DRAWING	
SHEET	2 OF 2
DATE	05-08-2000
DRAWN	TANG
	B-2317 REV 3.1