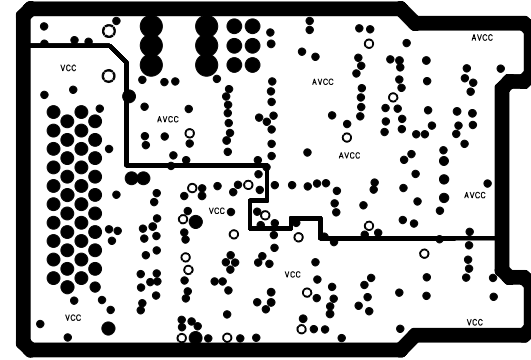
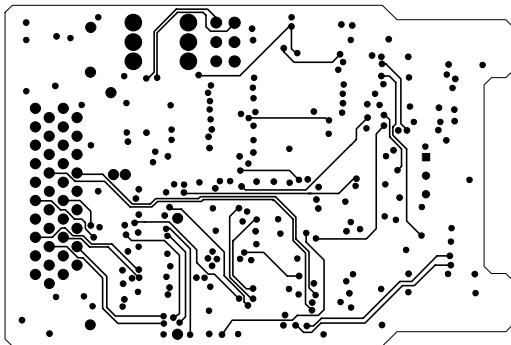


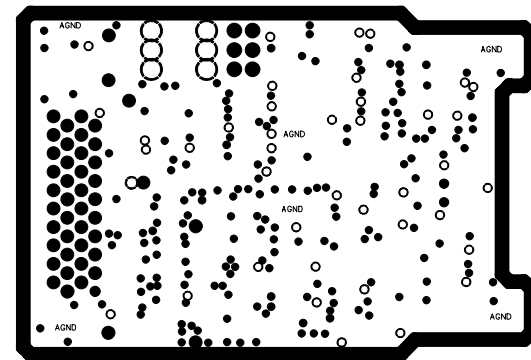
FILE1: LAYER1 (Top layer, SIGNAL_1)



FILM2: LAYER2 (VCC,AVCC)

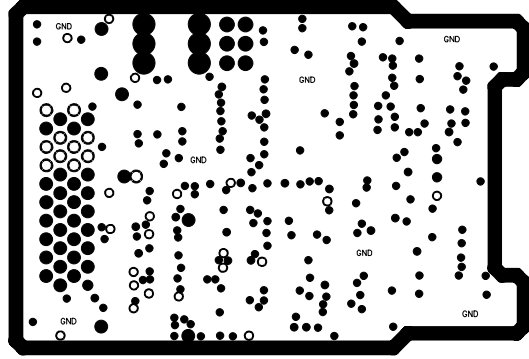


FILM3: LAYER3 (inner signal layer1, SIGNAL_3)

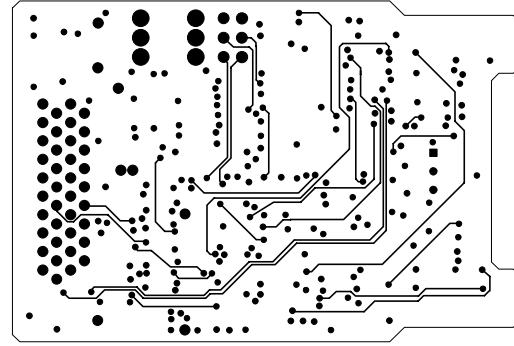


FILM4: LAYER4 (AGND)

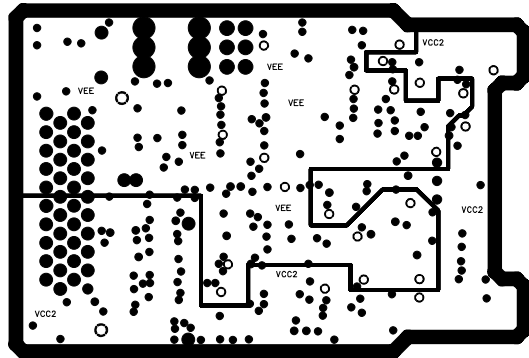
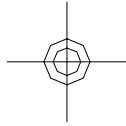
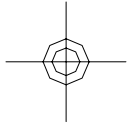
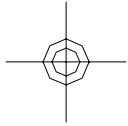




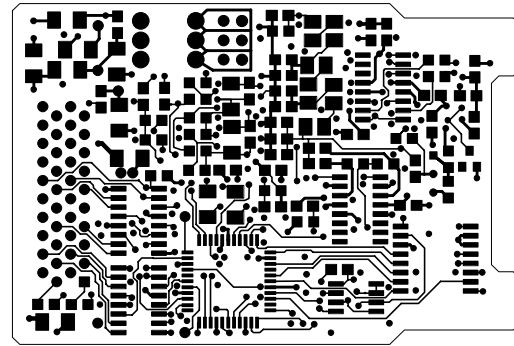
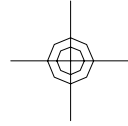
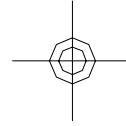
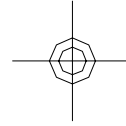
FILM5: LAYERS (GROUND)



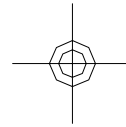
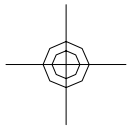
FILM6: LAYER6 (inner signal layer2, SIGNAL_4)

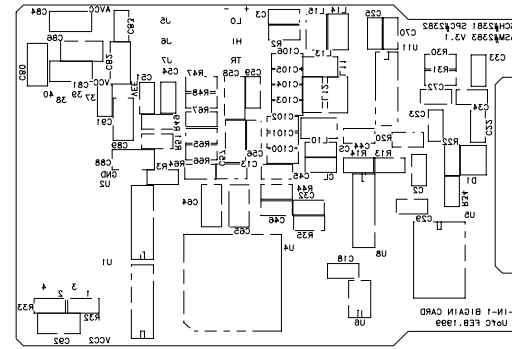
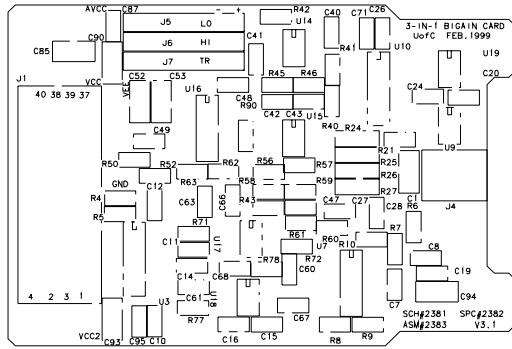


FILM7: LAYER7 (VEE, VCC2)



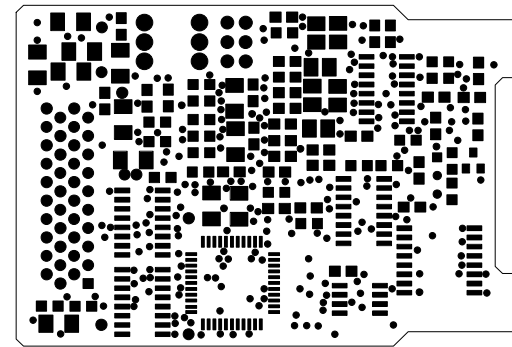
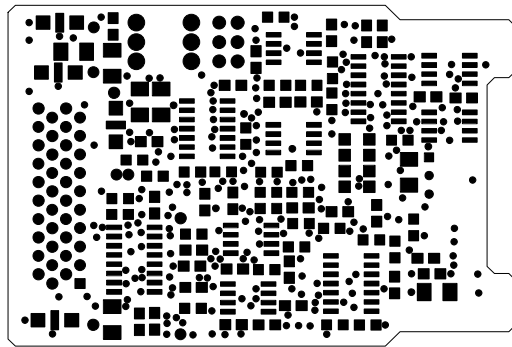
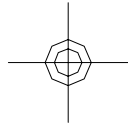
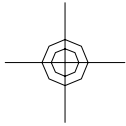
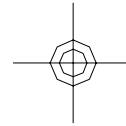
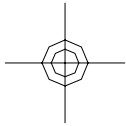
FILM8: LAYER8 (Bottom layer, SIGNAL_5)





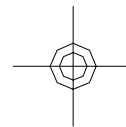
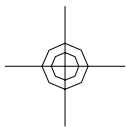
FILM9: Top Silkscreen (SILK_SCREEN_1)

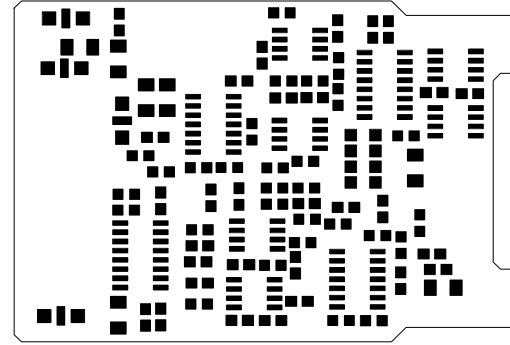
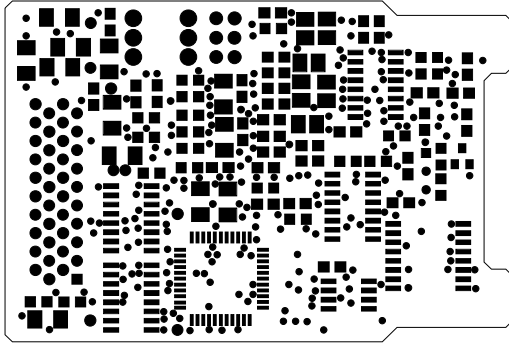
FILM10: Bottom Silkscreen (SILK_SCREEN_5)



FILM11: Top Solder Mask (SOLDER_MASK_1)

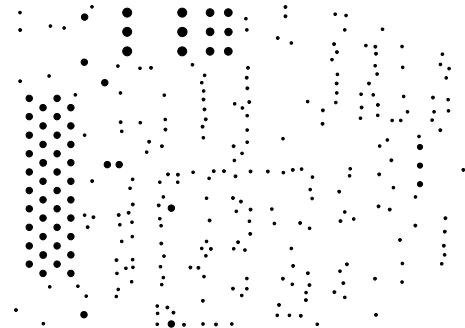
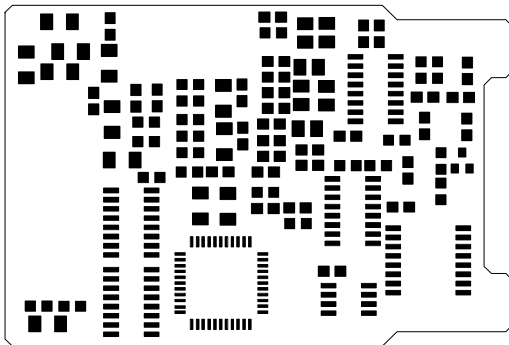
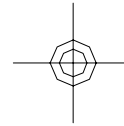
FILM12: Bottom Solder Mask (SOLDER_MASK_5)





FILM12: Bottom Paste Mask (PASTE_MASK_2)

FILM13: Top Paste Mask (PASTE_MASK_1)



FILM14: Bottom Paste Mask (PASTE_MASK_3)

