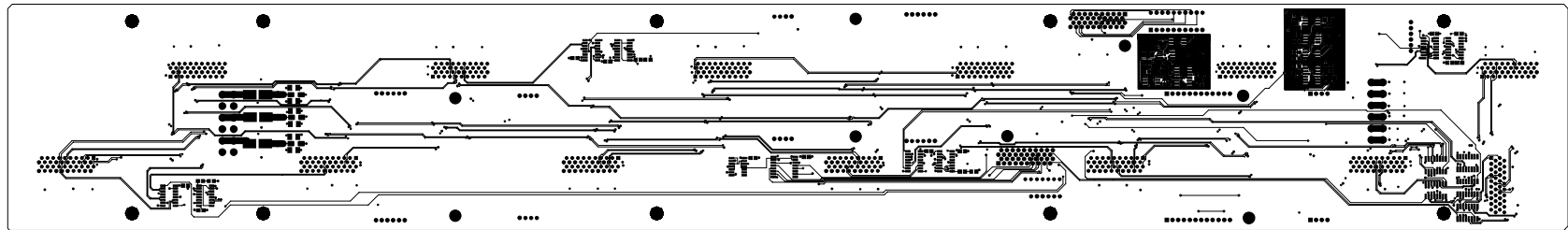
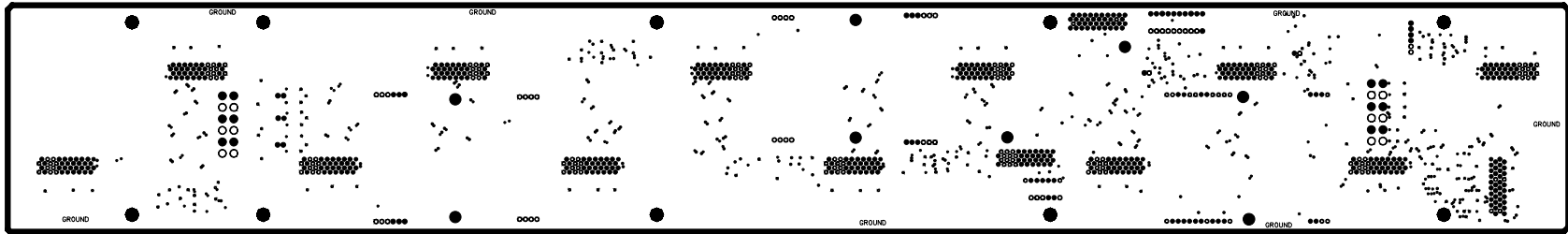


FILM2: LAYER2 (VCC2)

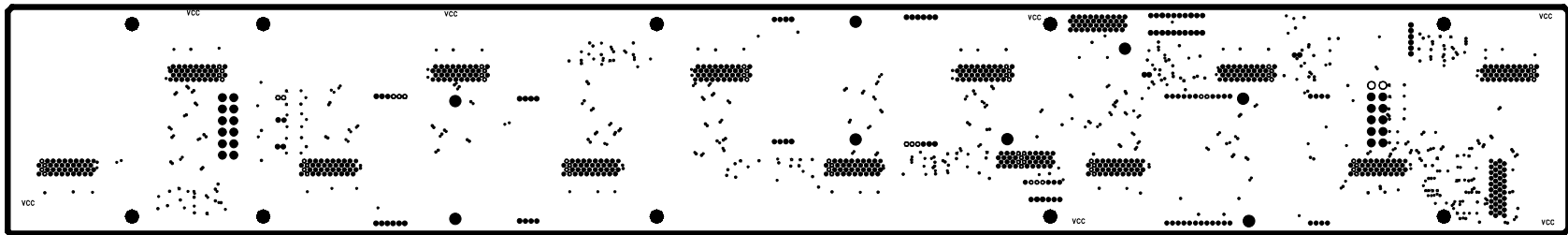


FILM1: Layer1 (Signal_1,Top Signal Layer)



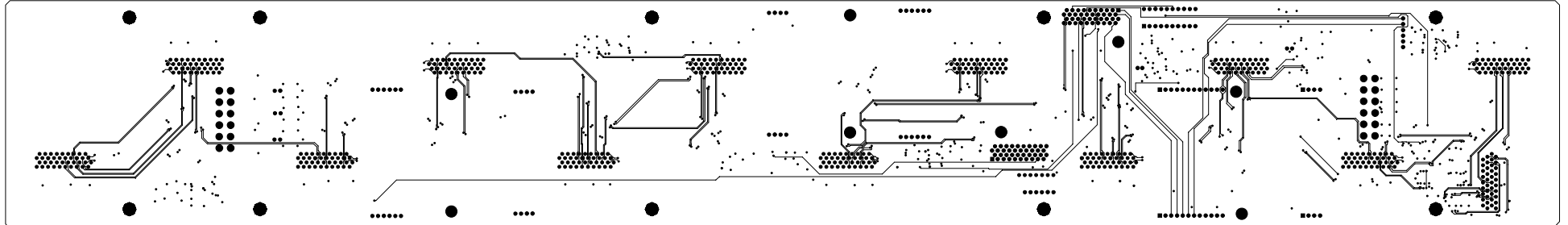
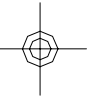


FILM4: LAYER4 (GROUND)

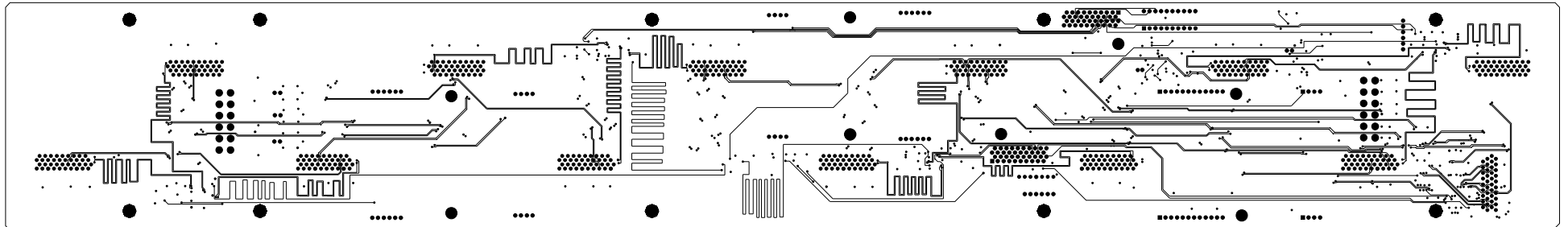
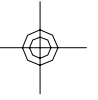
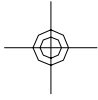
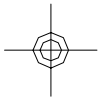


FILM3: LAYER3 (VCC)



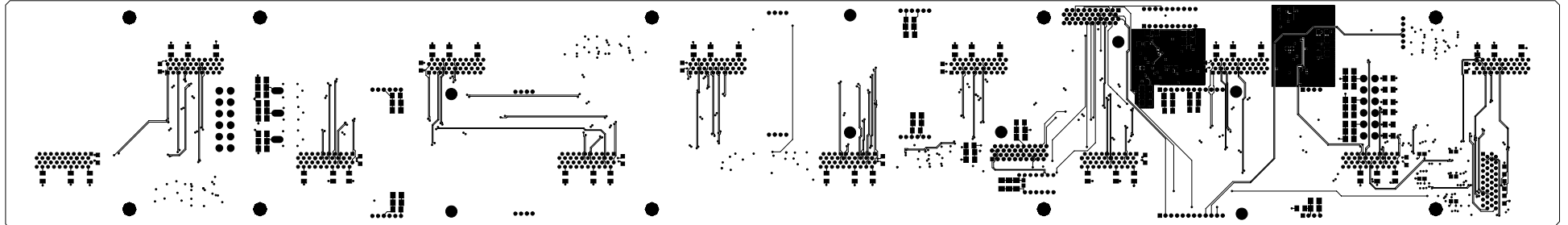
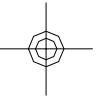


FILM6: LAYER6 (Signal_4)

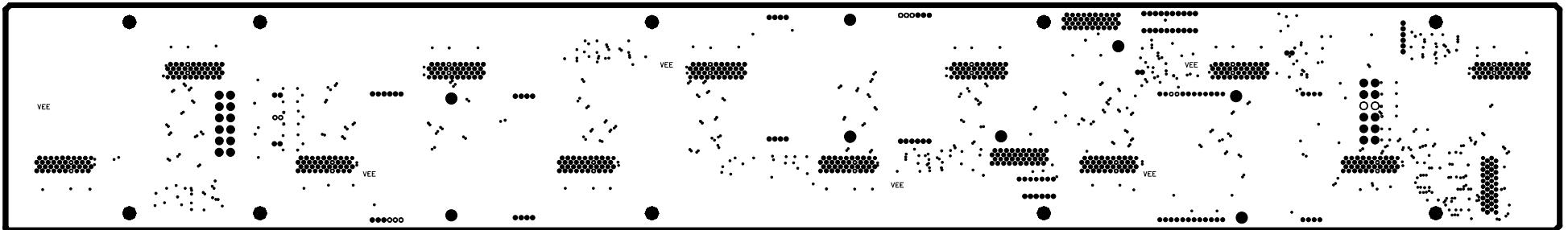
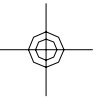


FILM5: LAYER5 (Signal_3)

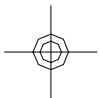


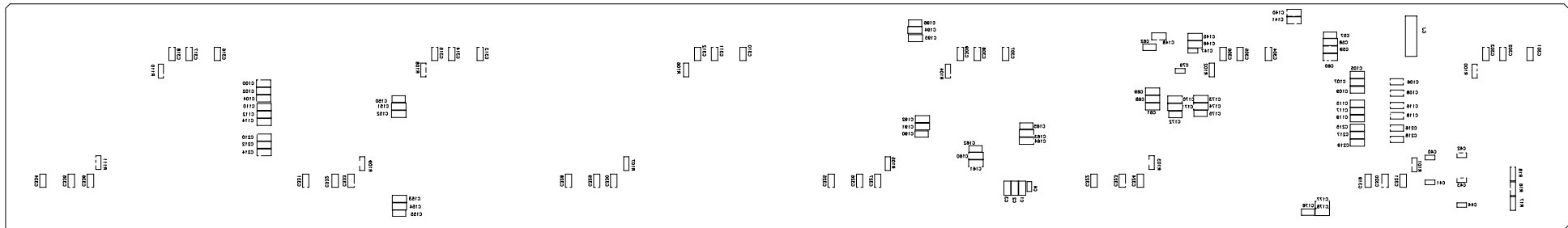
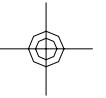
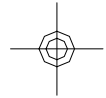


FILM8: Layer8 (Signal Layer)

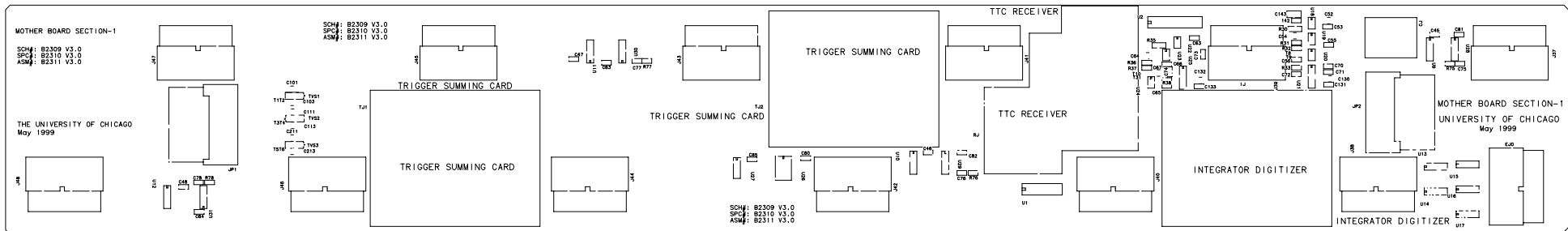
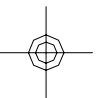
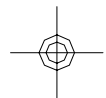
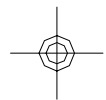


FILM6: LAYER6 (VEE)

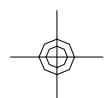


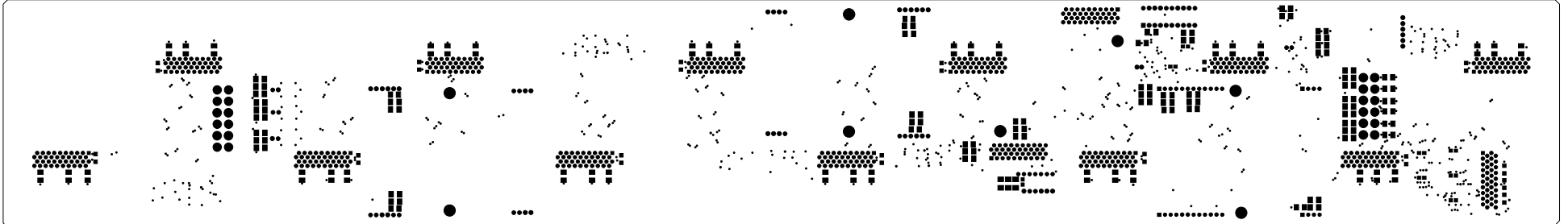


FILM10: (Bottom Silkscreen)

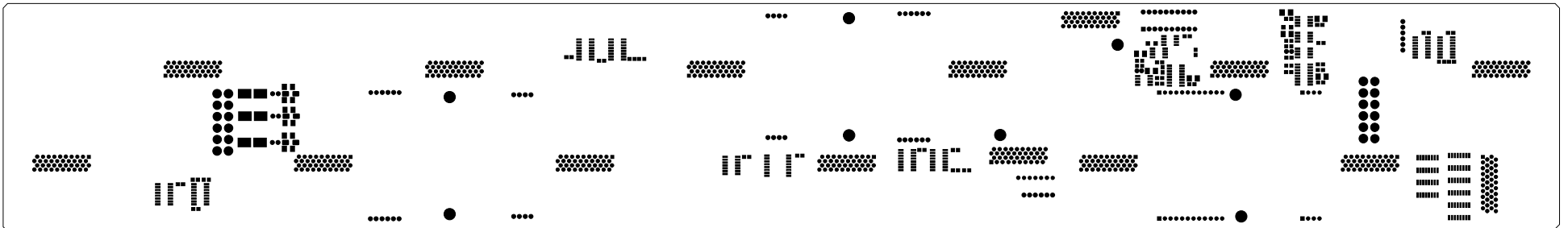


FILM9: (Top Silkscreen)



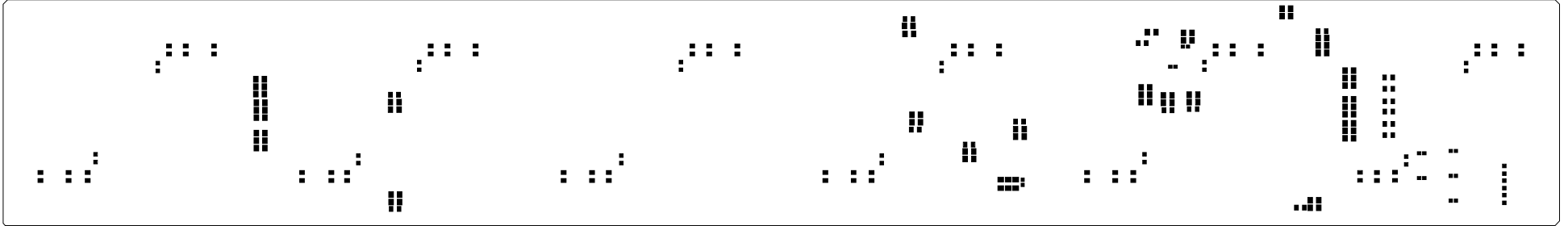


FILM12: (Bottom Solder Mask)

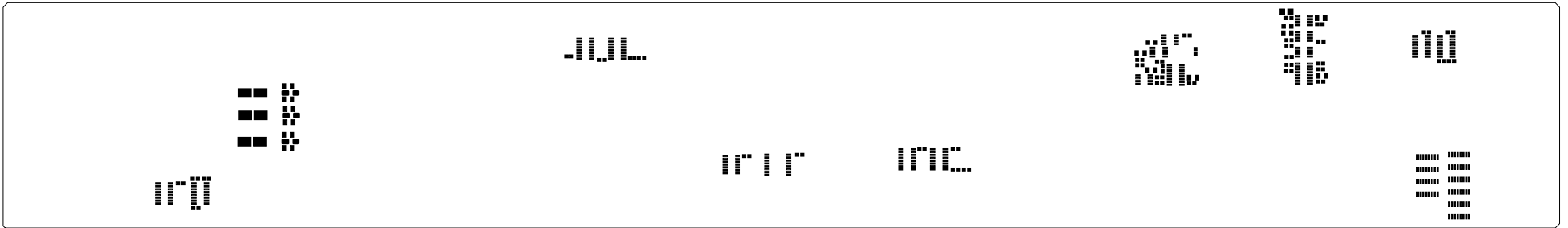


FILM11: (Top Solder Mask)





FILM14: (Bot Paste Mask)



FILM13: (Top Paste Mask)



