QC Sheet for 3-in-1 Cards

Date:										
Operator:										
Serial Number:										
	-	4								
			Limits		F	Pre-Burn-I	n	Р	ost-Burn-	In
Digital Tests					-			-		
Digital rests	Write/Read all registers	OK]			1				
			1			1				
Fast Pulse Tes	ts									
		Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.
		(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)
	High Gain	< 2	73-90	< 10	(0	(0.1.0) = 0 /	(0	(01110)	(0	(0)
	Low Gain	< 2	1.2-1.4	< 10						
	Over/undershoot @ 75 ns	< 5%	Ì			Ī				
		1	1		1	1				
Integrator Test	ts									
0		Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.
		(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)
	Gain 1	< 20		< 40						
	Gain 2	< 20		< 40						
	Gain 3	< 20		< 40						
	Gain 4	< 20		< 40						
	Gain 5	< 20		< 40						
	Gain 6	< 20		< 40						
	Output gate open/close	OK								
	Feed through for 4V	< 4 mV								
			•			•		-		
Trigger Output	Tests									
		Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.	Ped. Noise	Slope	Non-Lin.
		(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)	(cnts)	(cnts/pC)	(cnts)
	Analog Output	< 2	1.2-1.4	< 10						

Output gate open/close	OK
Gate feed through for 1 V	< 10 mV