ID	Task Name	Resource / Date
1	Backplanes/PS's/Rack Prot,/Cooling (L1+L2,8 racks)	Bob DeMaat
2	L1 Cables Pulled to Counting Room	Dervin Allen
3	96 Dirac Boards Tested at UC	Pedro Amaral
4	2 CrateSum Boards Tested at UC	Nancy Lai
5	12 Crate Sum Boards Tested at UC	Nancy Lai
6	PreFred SUMET Board Testing (UC)	Carla Pilcher
7	PreFred CALTRG Board Testing (UC)	Carla Pilcher
8	PreFred MUTRG Board Testing (UC)	Carla Pilcher
9	PreFred TRKTRG Board Testing (UC)	Carla Pilcher
10	96 Aux Cards Tested at UC	Ray Culbertson
11	96 Dirac Boards Installed at B0	Pedro Amaral
12	2 CrateSum Boards Installed at B0	Nancy Lai
13	12 CrateSum Boards Installed at B0	Nancy Lai
14	2 PreFred Boards Installed in B0	Carla Pilcher
15	4 PreFred Boards Installed in B0	Carla Pilcher
16	96 Aux Cards Delivered to B0	Ray Culbertson
17	L1 Cables Plugged in/Aux Cards Seated	Ray Culbertson
18	L1 Cable/Aux Card/Dirac Continuity Test	Pedro Amaral
19	Dirac/DCAS Cables Completed	Monica Tecchio
20	Dirac/DCAS Cables Installed	Pedro Amaral,Monica Tecchio
21	CrateSum to PreFred Cables Completed	Nancy Lai,Carla Pilcher
22	CrateSum to PreFred Cables Installed	Nancy Lai,Carla Pilcher
23	XTRP to Dirac Cables Fabrication	Ray Culbertson
24	XTRP to Dirac Cables Installed	Ray Culbertson
25	TSI/Clock/etc. Cables Completed	Peter Wilson
26	Level1 Hardware completely Installed	12/10
27		
28	Spec for Run Init Routine(s) (Hot,Warm?, and Cold)	Steve Vecjik
29	Spec for Trigger Data Base Routines	Steve Vecjik
30	Spec for Download from RunConditions DB	Steve Vecjik
31	Spec for Writing To RunConditions DB	Steve Vecjik
32	Spec for Error Handling Routines	Steve Vecjik
33	Spec for End Run (If Any)	Steve Vecjik
34	Spec for 'Stage 0' Threshold/HotChannel/Calib/Init/Run/Write Sequence	Steve Vecjik
35	Dirac/Cratesum/PreFred No-Download Init Code (from CDFVME)	Pedro Amaral,Nancy Lai,Carla Pilcher
36	Init Code With No DB Working in B0	11/17
37	L1 Crate Burn-in: simple init and readout 2 shifts/day	Pedro,Nancy,Carla,Steve Vecjik
38		
39	Code Templates for Data Base RW Access	Steve Vecjik,Pedro,Nancy,Carla
40	Code for Dirac/Cratesum/PreFred Full-Download Ready	Pedro Amaral,Nancy Lai,Carla Pilcher
41	Dirac/Cratesum/PreFred Full-Download Init from CDFVME (masks,threshold	11/30
42	Dirac/Cratesum/PreFred Full-Download Init from RunC (masks,thresholds,etc	12/3
43	TRIGSIM Code Development	
44	Full Run Startup, with TRIGSIM Error Monitoring, Error Handling, Run Summ	12/24
45	Cosmic Ray Run Begins	12/31

Timeline: October (10/3, 10/10, 10/17, 10/24), November (10/31, 11/7, 11/14, 11/21, 11/28), December (12/5, 12/12, 12/19, 12/26), January (1/2, 1/9, 1/16, 1/23), February (1/30, 2/6, 2/13, 2/20), March (2/27, 3/5, 3/12, 3/19, 3/26)

Project: Level 1 Installation
Date: Sun 10/17/99

Task | Progress | Milestone | Summary | Rolled Up Task | Rolled Up Milestone | Rolled Up Progress